AMENDMENTS TO THE CLAIMS

1. (Currently Amended) A method of manufacturing a plasma display panel ~~(PDP)~~ including a scan electrode, a sustain electrode, and an address electrode, said method comprising:

short-circuiting the scan electrode, the sustain electrode and the address electrode;

~~comprising a step of:~~

applying ~~to the address electrode~~ at least one of a first pulse voltage ~~for the address electrode~~ and a second pulse voltage ~~for~~to the address electrode; and~~electrode, in an aging step in which aging discharge is performed by~~

alternately applying a scan electrode pulse voltage ~~for the scan electrode~~ and a sustain electrode pulse voltage ~~for the sustain electrode~~ at least across the scan electrode and the sustain electrode, wherein the first pulse voltage has a first pulse voltage rising edge timing synchronized~~synchronizing~~ with a scan electrode pulse voltage rising edge timing ~~of the pulse voltage for the scan electrode~~ and a pulse width smaller than ~~that~~a width of the scan electrode pulse voltage ~~for the scan electrode~~, and the second pulse voltage ~~for the address electrode~~ has a second pulse voltage rising edge timing synchronized~~synchronizing~~ with a sustain electrode pulse voltage rising edge timing ~~of the pulse voltage for the sustain electrode~~ and a pulse width smaller than ~~that~~ a width of the sustain electrode pulse voltage ~~for the sustain electrode~~.

2. (Currently Amended) The method of manufacturing a ~~PDP~~plasma display device of claim 1 further comprising~~, wherein there is~~ at least one of ~~a period for~~ stopping application of the first pulse voltage ~~for the address electrode~~ to the address electrode for a period and ~~a period for~~ stopping application of the second pulse voltage ~~for the address electrode~~ to the address electrode for a period.

3. (Currently Amended) The method of manufacturing a ~~PDP~~plasma display device of claim 2 further comprising applying~~, wherein~~ the first pulse voltage less than four times successively to the address electrode~~for the address electrode~~ and applying the second pulse voltage less than four times successively~~for the address electrode are applied~~ to the address

electrode ~~so that the first pulse voltage is applied less than four times successively and the second pulse voltage is applied less than four times successively~~.

4. (Currently Amended) The method of manufacturing a ~~PDP~~plasma display device of claim 1 further comprising using~~, wherein~~ values of the first pulse voltage ~~for the address electrode~~ and the second pulse voltage that are less than or equal to~~for the address electrode do not exceed a value of the~~ a scan electrode pulse voltage value ~~for the scan electrode~~ and ~~a value of the~~ a sustain electrode pulse voltage value~~for the sustain electrode~~.

5. (Currently Amended) The method of manufacturing a ~~PDP~~plasma display device of claim 1 further comprising decreasing~~, wherein~~ a value of at least one of the scan electrode pulse voltage ~~for the scan electrode~~, the sustain electrode pulse voltage ~~for the sustain electrode~~, and the address electrode pulse voltage ~~for the address electrode is decreased~~ with time.

6. (Currently Amended) A method of manufacturing a plasma display panel including a scan electrode, a sustain electrode, and an address electrode, said method comprising: ~~comprising the steps of:~~

short-circuiting the scan electrode, the sustain electrode and the address electrode;

causing a first discharge ~~one of~~ between the scan electrode and the address electrode~~, and~~ or between the sustain electrode and the address electrode; and ~~using this discharge~~,

triggering a second discharge between the scan electrode and sustain electrode using the first discharge~~, in an aging step in which aging discharge is performed~~ by alternately applying a scan electrode pulse voltage ~~for the scan electrode~~ and a sustain electrode pulse voltage ~~for the sustain electrode~~ at least across the scan electrode and the sustain electrode.